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Docket No.: H0897

OCT & 2 2006

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Paul J. Steffan

: Confirmation No.: 2278

Serial No.:

10/770,711

: Art Unit:

2825

Filed:

2/2/2004

: Examiner:

Magid Y. Dimyan

For:

PREDICTING DEFECT

FUTURE EFFECTS IN

INTEGRATED CIRCUIT TECHNOLOGY

DEVELOPMENT TO

PACILITATE

SEMICONDUCTOR

WAFER LOT

DISPOSITION

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AMENDMENT AFTER FINAL REJECTION UNDER 37 C.F.R. §1.116

Sir/Madam:

The following Amendment and Remarks are submitted under 37 C.F.R. §1.116 in response to the Office Action mailed July 31, 2006, following the amendment format set forth under 37 CFR §1.121. After this introductory section, there are Amendments to the Claims and then Remarks, each starting on a separate page.

Amendments to the Claims consist of amendments for claims 1-20, which are incorporated in a complete listing of the claims.

Reconsideration of the objections and rejections is respectfully requested.